



GAU-2822 \$  
24-1-13-03  
PR

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Wendell P. Noble et al.

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

Docket No.: 303.379US2

Filed: April 17, 2000

Examiner: Michael Trinh

Serial No.: 09/551,027

Due Date: December 24, 2002

Group Art Unit: 2822

Commissioner for Patents  
Washington, D.C. 20231


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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (14 Pages).
- ☒ 1 sheet of red-line drawings.
- ☒ A Clean Version of Specification Paragraphs (1 pg.).
- ☒ A Clean Version of Pending Claims (8 pgs.).
- ☒ A Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 2 cited references.

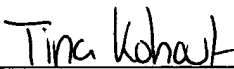
Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By:   
Atty: Viet V. Tong  
Reg. No. 45,416

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 23 day of December, 2002.

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